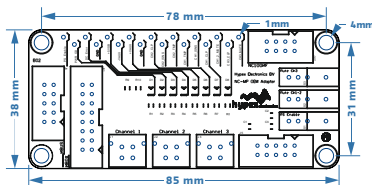


NCxxxMP series evaluation board



Highlights

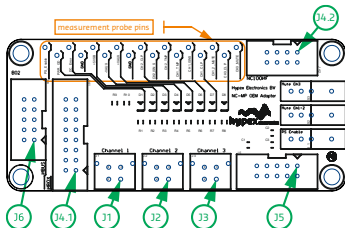
- Makes it easy to evaluate the NCxxxMP series with the optional NC100HF
- PS Enable switch
- Ch 1-2 mute switch
- Ch 3 (tweeter) mute switch
- H-Box connector
- H-Bus connector
- NC100HF half H-box connector

Introduction

This board is specifically designed to make it easy to evaluate the new NCxxxMP series mains powered NCORE modules. Furthermore the NC100HF can be easily connected using a 8-pin box interface cable. A 10-pin JST interface cable must be used to connect the NC100HF with the NCxxxMP.

It features three Molex Micro-Fit 3.0 connectors for audio input signals (Molex cable-end part no: 43025-0400).

The evaluation board comes with 10mm nylon spacers which can be used to mount the board using M3 screws.



Connectors

Connector	Remarks
J1	NCxxxMP Channel 1 input
J2	NCxxxMP Channel 2 input
J3	NC100HF input
J4.1	H-Box for NCxxxMP
J4.2	H-Box for NC100HF
J5	General Purpose Supply Output
J6	H-Bus for NCxxxMP

This evaluation board features additional measurement pads compatible with Farnell test pins: 1702010 or 1702006 or equivalent pins with 1mm pad diameter.

Pinout of the connectors can be found in the respective NCxxxMP and NC100HF datasheets.

For further details regarding specific IOs and performances, please refer to the datasheet.

Connector Pins

General Purpose Supply Output

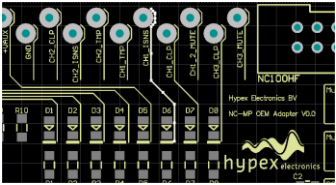
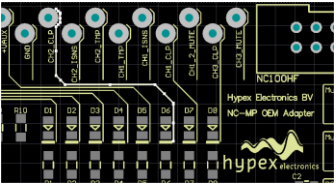
Pin	Remarks
J5.1	PS Enable
J5.2	Ground
J5.3	Vdc Standby
J5.4	Vdc Standby
J5.5	+Vaux
J5.6	+Vaux
J5.7	-Vaux
J5.8	-Vaux
J5.9	Ground
J5.10	Ground

Audio input J1, J2, J3.

Pin	Remarks
Jx.1	Non-inverting audio input
Jx.2	Inverting audio input
Jx.3	n.c.
Jx.4	Ground

1 Errata

On the black V0.0 boards there is a minor silkscreen error. This does not affect the pinout or function of the evaluation board, however to avoid confusion please find the following.

Black PCBA V0.0	Green PCBA 01xx
	
The silkscreen on this board connects D6 with CH1_ISNS. However D6 is an indicator for Ch2_CLP and therefore the silkscreen is not correct.	In the revised version 01xx the silkscreen has been corrected. Now the silkscreen connect D6 with Ch2_CLP. The text on both board is correct.

2 Revisions

Document revision	Module revision	Change log	Date
01	V0.0	Pre-release version	Jan '16
02	01xx	Release version	May '16
03	01xx	Irregularity in the audio input J1, J2, J3 corrected: differential signal inputs were swapped, resulting in a 180° phase shift.	Dec '16
04	02xx	Description Pin Jx.1 and Jx.2 incorrect	May '23

3 Disclaimer

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